

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng, *et al.* Docket No.: TSM02-0971
Filed: Herewith Examiner: TBD
Serial No.: TBD Art Unit: TBD
For: Method for Reducing Defects in Post Passivation Interconnect Process

Mail Stop: Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

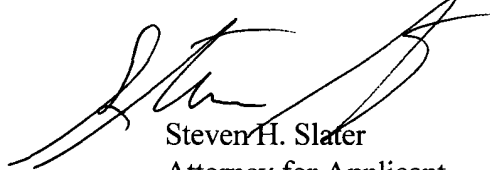
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Respectfully submitted,


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August 6, 2003
Date

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